File   View   Edit   Tools   Window   Help

- Drafts
- Pending
- Active
  - L1: (2) ("5513135").PN.
  - L2: (2159937) configur$6
  - L3: (1753076) width
  - L4: (865477) buffer$4
  - L5: (678505) module
  - L6: (22209) 2 and 3 and 4 and 5
  - L7: (108) 2 same 3 same 4 same 5
  - L8: (14) 2 with 3 with 4 with 5
- Failed
- Saved
- Favorites
- Tagged (1)
- UDC
- Queue
- Trash

	U	1	Document ID	Issue Date	Pages	Inventor	Title	Current OR	Cu
1	☐	☑	US 20040105292 A1	20040603	103	Matsui, Yoshinori	Memory system and data transmission method	365/63	365
									365

BRS form   IS&R form   Hits   Detals

Ready   NUM

File   View   Edit   Tools   Window   Help

- Drafts
- Pending
- Active
  - L1: (782614) buffer
  - L2: (31373) (memory adj module) or simm or dimm
  - L3: (9639) 1 and 2
  - L4: (3094) 1 same 2
  - L5: (2021) 1 with 2
  - L6: (1806) 1 near10 2
  - L8: (1447) 1 near5 2
  - L9: (3979) dimm or (dual adj inline adj memory adj module)
  - L10: (1441) 1 and 9
  - L11: (322) 1 same 9
- Failed
- Saved
- Favorites
- Tagged (1)
- UDC
- Queue
- Trash

U	1	Document ID	Issue Date	Pages	Inventor	Title	Current OR	Cu
1		US 5513135 A	19960430	20	Dell; Timothy J. et al.	Synchronous memory packaged in single/dual in line memory mod	365/52	365

BRS form   ISUB form   Hist   Details

ready

File   View   Edit   Tools   Window   Help

- Drafts
- Pending
- Active
  - L1: (4075) configur$6 near width
  - L2: (26442) memory adj module
  - L3: (41) 1 and 2
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

DBs: US-PGPUB, USPAT, EPO, JPO, DERWENT, IBM_TDB

Default operator: OR

☑ Plurals
☑ Highlight all hit terms instantly

1 and 2

File   View   Edit   Tools   Window   Help

BRS form   FSUB form   Hits   Details

ady